

IN THE SPECIFICATION

Please insert the following paragraph on page 2, before the Field of the Invention:

REFERENCE TO RELATED APPLICATIONS

This application is a continuation of Application No. 10/269,364, filed October 11, 2002, which is a continuation of U.S. Patent No. 6,507,217, filed September 13, 2001, which is a continuation of U.S. Patent No. 6,433,580, filed March 2, 1998, which is a continuation of Application No. 08/484,922, filed June 7, 1995, which is a continuation of U.S. Patent No. 5,457,410 filed August 3, 1993, which are all herein incorporated by reference.

On page 19, replace lines 1-15 with the following amended paragraph.

Referring now to Figure 3A, a logical cluster 107 is shown. In the currently preferred embodiment, logical cluster 107 is comprised of four cells 301-304 and a D flip-flop 305, twenty five switches 306-330, and five intraconnections lines 331-335. D flip flop 305 and cell 304 form a cell 361, such as cell 250 described with respect to Figure 2a. The Intraconnection lines 331-335 and switches 306-330 form the I-Matrix. I-Matrix provide connectability of the output, X, of each of the four cells 301-304, and the output X of the D flip-flop 305 to at least one input of each of the other three cells and the D flip-flop. For example, the output X of cell 301 can be connected to input A of cell 302 by enabling switches 306 and 307. Likewise, the output X of cell 301 can be connected to input B of cell 303 by enabling switches 306 and 310. Output X of cell 301 can be connected to input A of cell 304 by enabling switches 306 and 308. Output X of cell 301 can be connected to input D of the D flip-flop cell 305 by enabling switches 306 and 309.